

KUMBAM PAVAN REDDY
Phone number:- 8639861836
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PCB Designer

OBJECTIVE

I have extensive experience in PCB designing, including schematic capture, layout optimization, I specialize in high-speed design, multi-layer PCB layouts, and ensuring compliance with industry standards for reliable and efficient electronic circuits. My expertise also includes selecting appropriate components, minimizing electromagnetic interference, and collaborating with hardware engineers to bring innovative designs to life.

EXPERIENCE

Electro Circuit Systems Pvt. Ltd., Hyderabad {03/2024 – Present}

- Designed 2 to 8 layer PCBs for consumer electronics and IoT devices using Pads Standard, ORCAD Cadence, Pads Professional Designer
- Collaborated with electrical engineers to convert schematics into production-ready layouts
- Generated Gerber files, drill files, and assembly drawings for manufacturing
- Conducted DRC and ERC checks to ensure reliability and compliance with IPC standards
- Assisted in component footprint creation and library management
- Worked with fabrication vendors to resolve manufacturing issues

CERTIFICATIONS

- PCB Design certification from NSIC-Hyd
- EV workshop certification from NSIC-Hyd

PROJECTS

- **Power Supply PCB for Analog Circuits** --- Designed and developed a **linear power supply PCB** specifically for **low-noise analog circuits**, ensuring stable power delivery with minimal voltage ripple.
- **Medical Device PCB Design** ---- Designed a **multi-layer PCB** for medical applications, ensuring compliance with industry standards for reliability, precision, and safety.

ACADEMIC RECORD

- Course - B.Tech in Electronics and Communication Engineering
- University - VIGNANA BHARATHI INSTITUTE OF TECHNOLOGY AUSHAPUR

- Course - INTERMEDIATE (MPC)
- University - LITTLE FLOWER JUNIOR COLLEGE

- Course - SSC
- Institute - MUDRA CONCEPT SCHOOL

SKILLS

- CAD Tools: PADS Standard, Cadence Orcad, PADS Professional, Cam350.
- PCB Design: Schematic capture, multilayer PCB layout, component placement, routing
- Standards & Practices: IPC standards, DFM/DFT principles
- Tools & Technologies: Gerber file generation, Panel creation, solder mask/paste layer setup
- Communication: Cross-functional collaboration with hardware and mechanical teams
- Scripting/Automation (if any): Basic Python for file handling (optional)

PERSONAL PROFILE

- Date Of Birth : 21/11/2000
- Gender : Male
- Marital status : Single
- Father's Name : Kumbam Venugopal reddy
- Address : Hno- 2-106/2 Mallikarjuna Nagar colony
Uppal depot peerzadiguda 500092 .
- Languages Known : Telugu, English, Hindi

"I hereby declare that the information provided in this resume is true and correct to the best of my knowledge and belief."

Place:Hyderabad

(K. PAVAN REDDY)